



1374.42869TRN

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: CHAKIHARA, et al.
Serial No.: 10/629,733
Filed: July 30, 2003
For: A SEMICONDUCTOR MEMORY DEVICE AND A METHOD OF
MANUFACTURING THE SAME, A METHOD OF
MANUFACTURING A VERTICAL MISFET AND A VERTICAL
MISFET, AND A METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR
DEVICE
Group: 2826
Examiner: F. ABRAHAM

SUPPLEMENTAL AMENDMENT

MS: AMENDMENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 29, 2005

Sir:

In supplement to the Amendment filed on May 31, 2005 and the Information
Disclosure Statement filed on June 8, 2005 please further amend the above-
identified application as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the Amendments.

08/02/2005 MBEYENE1 00000012 10629733

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